

**ABSTRACT**

A circuit is provided for limiting the in-rush current of a radio device coupled to a low-power external power source and includes a switch circuit in series between the power source and the radio device, the switch having an “off” state with a high impedance and an “on” state with a low impedance. Also included is a time-delay shorting circuit coupled to the switch circuit, the time-delay shorting circuit having a time constant. In operation, before the time constant elapses, the switch circuit is in the high impedance “off” state for limiting the in-rush current to the radio device and after the time constant elapses, the switch circuit is in the low impedance “on” state so that the radio device is powered by the external power source.